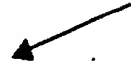


FIG. 13A

Subject Code

200



```
LABEL1    r3 := LOAD(r2 + 24)
          r1 := r1 -1
          CMP r1 0
          BC LABEL1
          STORE 16(r5) r3
```

FIG. 13B

Group Block

202

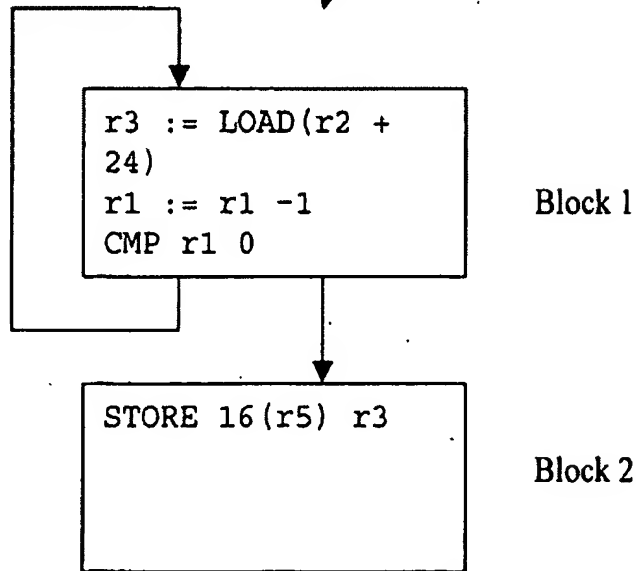


FIG. 13C

Block 1 IR Without Lazy Byteswapping

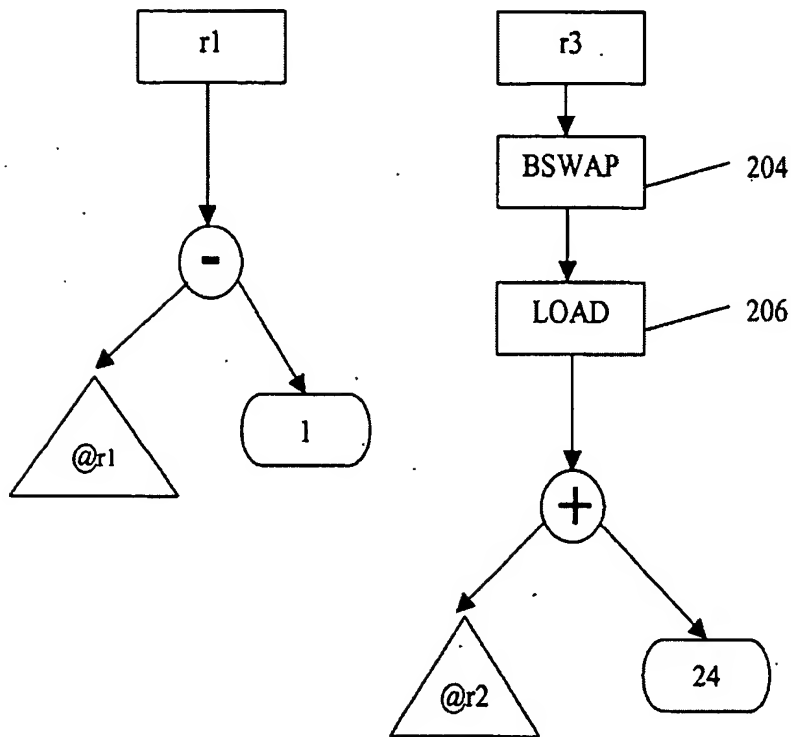


FIG. 13D

Block 2 IR Without Lazy Byteswapping

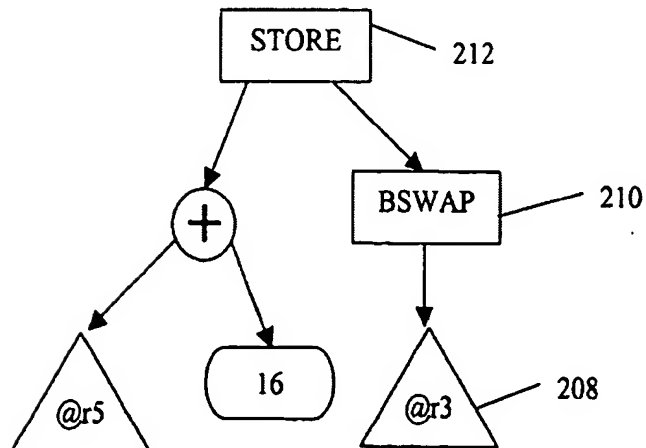


FIG. 13E

FIG. 13F

Block 2 IR With Lazy Byteswapping

